



Ampere is leveraging the depth and expertise within the cloud and semiconductor industries to push the boundaries of emerging cloud applications. Our world class team of scientists are focused on the development of new semiconductor designs and building out the first software ecosystem for Arm®-based server processors. We give our customers the freedom to challenge the status quo and accelerate next-generation data centers for the most memory-intensive applications.

Ampere Computing's Headquarters are located in Santa Clara, California, USA. Sales and Engineering offices are located throughout the world.

Vietnam Design Center provides good compensation and benefit programs and working conditions.

Company website: <http://www.amperecomputing.com>

Please forward resumes to [vnjobs@amperecomputing.com](mailto:vnjobs@amperecomputing.com)

## **Validation Engineer I & II**

### **Job Description**

Design and develop standalone application for ARM processor and industry bus interface such as RS-232, SATA, PCIE, DDR, Memory and Flash, USB, Ethernet, etc... Develop applications which are Operation System (OS) and technology independence and have the ability to perform self diagnostic and performance analysis.

### **Responsibilities**

- Engage in Chip bring-up phase, implement test cases according to test plan, debugging test in simulation and hardware environment, and verify test coverage according to IP specification.
- Working closely with US Design and Verification Engineering for problem analysis and resolution

### **Qualifications**

- BS/MS/Ph.D in Computer Engineering/Electrical Engineering or equivalent
- Good understanding on CPU-base architecture
- Software skill in C

- Have some basic working knowledge of RS-232, SPI, I2C, EPROM, SRAM, Flash Memory, etc...
- Some experience in Microcontroller/Embedded System Programming is desirable
- Good Debugging Skills
- Good written and communication skill in English and Vietnamese

**Additional skills:**

Any of the following is desirable but not required:

- U-Boot and embedded Linux Programming.
- TCL or Perl.
- Knowledge of or working experience on PCIE/USB/SATA/SDIO interfaces
- IXIA, Leroy PCIE, SATA and USB Test equipments
- Ethernet and TCP/IP related protocols.

**Location**

AMCC Design Center in Ho Chi Minh City, Vietnam.